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| **P.V.P Siddhartha Institute of Technology(Autonomous)** | **Signature of Invigilator with date:** | **Marks Obtained:** |
| **Department of Computer Science and Engineering** |
| **Course: B.Tech** | **Year: II** | **Semester: I** | **Objective: II** |
| **Regulation:PVP23** | **Maximum Marks:10Marks** | **Session: F.N** |
| **A.Y:2024-25** | **Date:27-11-2024** | **Duration: 20 min** |
| **Subject Code: 23ES1304** | **Subject Name: Digital Logic & Computer Organization** |
| **Registered Number:** | **Name:** |
| **Answer all the Questions. Each Question carries 2 Mark 5\*2M=10M** |
|  |
| **Q.No** | **Question** | **Marks** | **CO** | **Level** |
|  | Draw the flowchart for signed magnitude addition and subtraction. | **2M** | **CO1** | **L2** |
|  | Convert the following expression to Reverse Polish Notation(3\*4)+(5\*6). | **2M** | **CO1** | **L2** |
|  | What is Address Space and Memory Space. | **2M** | **CO1** | **L2** |
|  | What is Hit Ratio? | **2M** | **CO1** | **L2** |
|  | What is polling? | **2M** | **CO1** | **L2** |

Course Coordinator

Module Coordinator Program Coordinator