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| **P.V.P Siddhartha Institute of Technology** |
| **Department of Computer Science and Engineering** |
| **Course: B.Tech.** | **Year: III** | **Semester: II** | **A.Y:2024–25** | **Date:10-01-2025** |
| **Subject Code:** 20**CS3601** | **Subject Name: Compiler Design** | **Regulation:PVP20** |
| **QUESTION BANK FOR ASSIGNMENT-1** |
| **Q. No** | **QUESTION** | **CO** | **Level** |
| **1** |  | Explain the diagrammatic representation of a language processing system. | CO1 | L2 |
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| **2** |  | Explain the role and functions of lexical analyzer with the possible error recovery actions. | CO1 | L2 |
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| **3** |  | Examine whether the following grammar is LL(1) or not. S→Aa|bAc|Bc|bBa A→d  B→d | CO5 | L4 |
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| **4** |  | Obtain the stack implementation of shift reduce parser for the input string id1+ id2\*id3 for the following grammar E→E+E E→E\*E E→ (E) E→id and check whether the string can be parsed or not | CO5 | L4 |
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| **5** |  | Classify various parsing techniques | CO5 | L4 |
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| **6** |  | Inspect whether the following grammar is LL(1) or not A → ABd | Aa | a B → Be | b | CO5 | L4 |
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