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| **P.V.P SIDDHARTHA INSTITUTE OF TECHNOLOGY (AUTONOMOUS)** |
| **BRANCH : Computer Science and Engineering** | **REGULATION : PVP23** |
| **Course: B.Tech** | **SUBJECT : Digital Logic & Computer Organization** |
| **SubjectCode:23ES1304** | **Year and Semester: II Year / I Sem** | **Section: I/II/III** |
| **Academic Year:2024-25 (Semester-I)** |
| **ASSIGNMENT-II** |

**Assignment: Students are given to create a video presentation on a specific topic.**

**SECTION S2**

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| **Roll Number** | **Topic of video presentation** |
| 23501A0567 | Booth Multiplication algorithm  |
| 23501a0568 | MULTIPLICATION OPERATION ON SING MAGNITUDE DATA |
| 23501A0569 | Stack Organisation  |
| 23501A0570 | Auxiliary Memory |
| 23501a0571 | Memory hierarchy  |
| 23501A0572 | Booths multiplication algorithm  |
| 23501a0573 | Multiplication algorithm  |
| 23501A0574 | Associative Memory |
| 23501a0575 | Multiplication by using signed magnitude  |
| 23501A0577 | Instruction Format |
| 23501A0578 | Memory Hierarchy |
| 23501A0579 | Booth's Algorithm  |
| 23501A0580 | Auxiliary memory |
| 23501A0581 | Addition and Subtraction of binary numbers  |
| 23501A0582 | ASSOCIATIVE MEMORY |
| 23501A0583 | Cache Memory |
| 23501A0584 | ASSOCIATIVE MEMORY |
| 23501A0585 | Booth's Multiplication  |
| 23501A0586 | Booth multiplication  |
| 23501a0587 | chache memory |
| 23501A0588  | Stack organization(Register stack) |
| 23501a0589 | CACHE MEMORY |
| 23501A0590 | Associative memory |
| 23501a0591 | Memory stack |
| 23501a0593 | Associative memory  |
| 23501A0594 | General Register Organization  |
| 23501A0595 | Cache memory  |
| 23501A0596 | Instruction Formats |
| 23501A0597 | Instruction Format |
| 23501A0598  | Main Memory  |
| 23501A0599 | Addition and subtraction using 2's sign magnitude  |
| 23501a05a0 | logic gates |
| 23501A05A2 | Direct Memory Access(DMA) |
| 23501A05A3 | Associative Memory |
| 23501A05A4 | Auxiliary memory |
| 23501A05A5 | Associative Memory |
| 23501a05a6 | Addition and subtraction of Signed Magnitufe |
| 23501A05A7 | Instruction formats |
| 23501a05a8 | addition and subtraction by signed magnitude data |
| 23501a05a9 | General register organization  |
| 23501A05B0 | Booth multiplication algorithm  |
| 23501A05B1 | Instruction Format |
| 23501A05B2 | MULTIPLICATION USING SIGNED MAGNITUTE |
| 23501a05b3 | associative memory |
| 23501a05b4 | Multiplication algorithm |
| 23501a05b5 | Addition and subtraction using signed magnitude |
| 23501A05B6 | Cache memory |
| 23501A05B7 | Booth's Multiplication  |
| 23501a05b8 | multiplication using booth algorith |
| 23501a05b9 | Stack Organization  |
| 23501a05c0 | Memory heirchey |
| 23501A05C1 | Modes of Transfer  |
| 23501a05c3 | Booth multiplication |
| 23501A05C4 | Addition and Subtraction using Sign Magnitude  |
| 23501A05C5 | Addition and subtraction of binary numbers  |
| 23501A05C6 | Instruction format |
| 23501A05C7  | Input & Output devices |
| 23501A05C8 | Decimal arithmetic unit  |
| 23501A05C9 | Multiplication algorithm using signed magnitude  |
| 23501A05D0  | Cache Memory |
| 23501A05D1 | BOOTHS MULTIPLICATION  |
| 23501A05D2 | Associative Memory  |
| 23501A05E8 | Multiplication using Booths Algorithm |
| 23501AO5C2 | Booth's multiplication  |
| 24505A0507 | Booth's multiplication algorithm  |
| 24505A0508 | Cache Memory |
| 24505A0509  | Main memory  |
| 24505A0510 | Addition and Subtraction using signed magnitude and 2's complement |
| 24505A0511 | Associative Memory |
| 24505a0512 | Virtual Memory |

Course Coordinator

Module Coordinator Program Coordinator